

General Description

1.4 Mask Options

The M68HC05E5 has four mask options:

1. STOP instruction (enable/disable)
2. $\overline{\text{IRQ}}$ (edge-sensitive only or edge- and level-sensitive)
3. COP watchdog timer (enable/disable)
4. CPI Rate (1 second, 0.5 second, or 0.25 second)

NOTE: A line over a signal name indicates an active low signal. For example, $\overline{\text{RESET}}$ is active low.

1.5 Functional Pin Description

Figure 1-2 shows the single-chip mode pinout for the MC68HC05E5. Refer to the following subsections for a description of the pins.

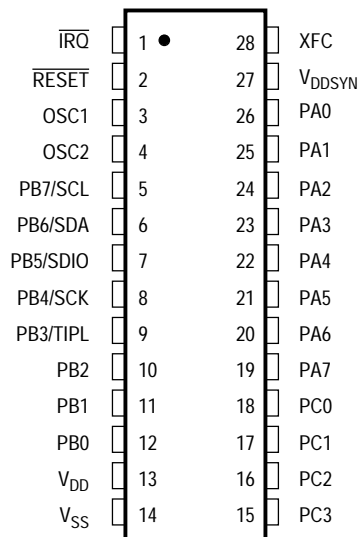


Figure 1-2. Single-Chip Mode Pinout