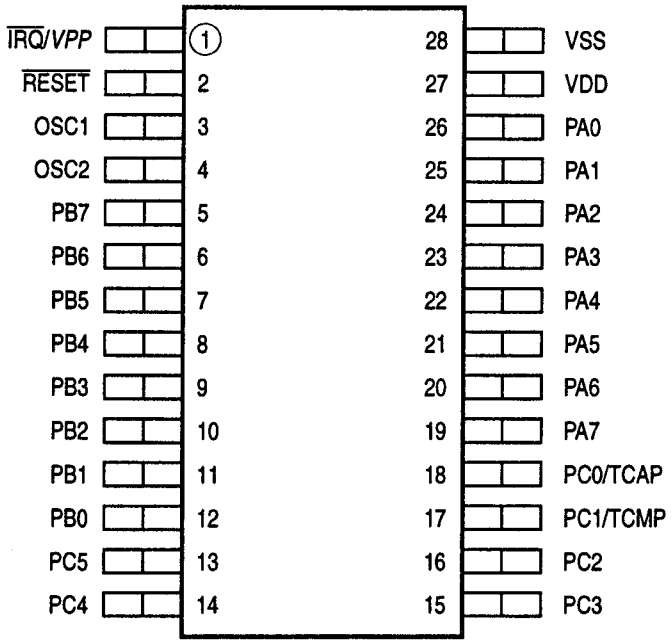

Figure 2-3 28-pin SOIC pinout

noise problems, special care must be taken to provide good power supply bypassing at the MCU. Bypass capacitors should have good high-frequency characteristics and be as close to the MCU as possible. Bypassing requirements vary, depending on how heavily the MCU pins are loaded.

2.4.2 $\overline{\text{RESET}}$

This active low input pin is used to reset the MCU. Applying a logic zero to this pin forces the device to a known start-up state. An external RC-circuit can be connected to this pin to generate a power-on reset (POR) if required. In this case, the time constant must be great enough (at least 100ms) to allow the oscillator circuit to stabilize. This input has an internal Schmitt trigger to improve noise immunity.

2.4.3 $\overline{\text{IRQ}}$ /*VPP*

This is an input-only pin for external interrupt sources. A mask option selects interrupt triggering to be either falling-edge sensitive or falling-edge-and-low-level sensitive. *For the MC68HC705P3, this pin also serves as the input for the EPROM programming voltage (VPP).*